keypad arrangement. The keypad **30** may also include various soft keys with associated functions. In addition, or alternatively, the mobile terminal **10** may include an interface device such as a joystick or other user input interface. Some embodiments employing a touch screen display, as described further below, may omit the keypad **30** and any or all of the speaker **24**, ringer **22**, and microphone **26** entirely. The mobile terminal **10** further includes a battery **34**, such as a vibrating battery pack, for powering various circuits that are required to operate the mobile terminal **10**, as well as optionally providing mechanical vibration as a detectable output.

[0038] The mobile terminal **10** may further include a user identity module (UIM) **38**. The UIM **38** is typically a memory device having a processor built in. The UIM **38** may include, for example, a subscriber identity module (SIM), a universal integrated circuit card (UICC), a universal subscriber identity module (USIM), a removable user identity module (R-UIM), etc. The UIM **38** typically stores information elements related to a mobile subscriber. In addition to the UIM **38**, the mobile terminal **10** may be equipped with memory. For example, the mobile terminal **10** may include volatile memory **40**, such as volatile Random Access Memory (RAM) including a cache area for the temporary storage of data. The mobile terminal **10** may also include other non-volatile memory **42**, which may be embedded and/or may be removable. The memories may store any of a number of pieces of information, and data, used by the mobile terminal **10** to implement the functions of the mobile terminal **10**.

[0039] An example embodiment of the invention will now be described with reference to FIG. **2**, which depicts certain elements of an apparatus **50** that provides for receipt of indirect touch input to a touch screen display. The apparatus **50** of FIG. **2** may be employed, for example, with the mobile terminal **10** of FIG. **1**. However, it should be noted that the apparatus **50** of FIG. **2** may also be employed in connection with a variety of other devices, both mobile and fixed, and therefore, embodiments of the present invention should not be limited to application on devices such as the mobile terminal **10** of FIG. **1**. For example, the apparatus **50** may be employed on a watch, a personal computer, a tablet, a mobile telephone, a large, stand-alone, mountable display, or other user terminal Moreover, in some cases, part or all of the apparatus **50** may be on a fixed device such as a server or other service platform, and the content may be presented (e.g., via a server/client relationship) on a remote device such as a user terminal (e.g., the mobile terminal **10**) based on processing that occurs at the fixed device.

[0040] It should also be noted that while FIG. **2** illustrates one example of a configuration of an apparatus **50** for receiving indirect touch input, numerous other configurations may also be used to implement embodiments of the present invention. As such, in some embodiments, although devices or elements are shown as being in communication with each other, hereinafter such devices or elements should be considered to be capable of being embodied within a same device or element and, thus, devices or elements shown in communication should be understood to alternatively be portions of the same device or element.

[0041] Referring now to FIG. **2**, the apparatus **50** may include or otherwise be in communication with a processor **70**, a user interface transceiver **72**, a communication interface **74**, and a memory device **76**. In some embodiments, the processor **70** (and/or co-processors or any other processing circuitry assisting or otherwise associated with the processor **70**) may be in communication with the memory device **76** via a bus for passing information among components of the apparatus **50**. The memory device **76** may include, for example, one or more volatile and/or non-volatile memories. In other words, for example, the memory device **76** may be an electronic storage device (e.g., a computer readable storage medium) comprising gates configured to store data (e.g., bits) that may be retrievable by a machine (e.g., a computing device like the processor **70**). The memory device **76** may be configured to store information, data, content, applications, instructions, or the like for enabling the apparatus to carry out various functions in accordance with an example embodiment of the present invention. For example, the memory device **76** could be configured to buffer input data for processing by the processor **70**. Additionally or alternatively, the memory device **76** could be configured to store instructions for execution by the processor **70**.

[0042] The apparatus **50** may, in some embodiments, be a mobile terminal (e.g., mobile terminal **10**) or a fixed communication device or computing device configured to employ an example embodiment of the present invention. However, in some embodiments, the apparatus **50** may be embodied as a chip or chip set. In other words, the apparatus **50** may comprise one or more physical packages (e.g., chips) including materials, components and/or wires on a structural assembly (e.g., a baseboard). The structural assembly may provide physical strength, conservation of size, and/or limitation of electrical interaction for component circuitry included thereon. The apparatus **50** may therefore, in some cases, be configured to implement an embodiment of the present invention on a single chip or as a single "system on a chip." As such, in some cases, a chip or chipset may constitute means for performing one or more operations for providing the functionalities described herein.

[0043] The processor **70** may be embodied in a number of different ways. For example, the processor **70** may be embodied as one or more of various hardware processing means such as a coprocessor, a microprocessor, a controller, a digital signal processor (DSP), a processing element with or without an accompanying DSP, or various other processing circuitry including integrated circuits such as, for example, an ASIC (application specific integrated circuit), an FPGA (field programmable gate array), a microcontroller unit (MCU), a hardware accelerator, a special-purpose computer chip, or the like. As such, in some embodiments, the processor **70** may include one or more processing cores configured to perform independently. A multi-core processor may enable multiprocessing within a single physical package. Additionally or alternatively, the processor **70** may include one or more processors configured in tandem via the bus to enable independent execution of instructions, pipelining and/or multithreading.

[0044] In an example embodiment, the processor **70** may be configured to execute instructions stored in the memory device **76** or otherwise accessible to the processor **70**. Alternatively or additionally, the processor **70** may be configured to execute hard-coded functionality. As such, whether configured by hardware or software methods, or by a combination thereof, the processor **70** may represent an entity (e.g., physically embodied in circuitry) capable of performing operations according to an embodiment of the present invention while configured accordingly. Thus, for example, when the processor **70** is embodied as an ASIC, FPGA or the like, the processor **70** may be specifically configured hardware for conducting the operations described herein. Alternatively, as